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(54) **OLED PIXEL STRUCTURE AND METHOD FOR MANUFACTURING THE SAME, OLED DISPLAY PANEL AND OLED DISPLAY DEVICE**

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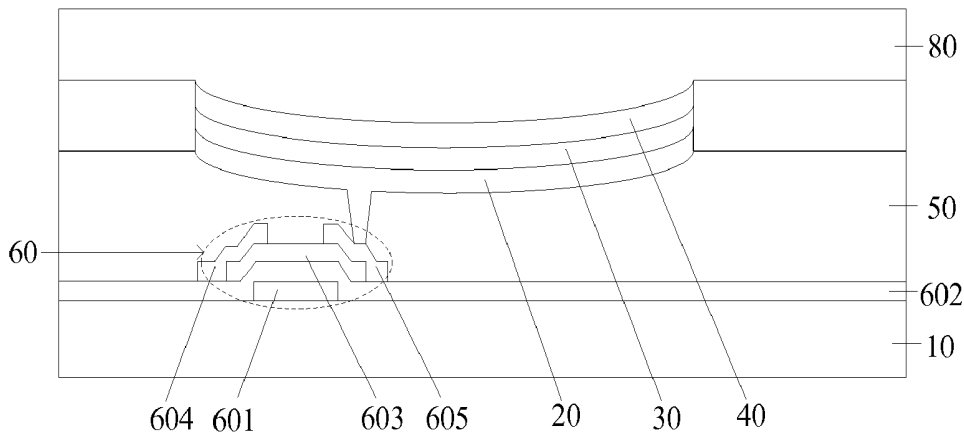
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(57) **ABSTRACT**

Embodiments of the present disclosure relate to an OLED pixel structure and a method for manufacturing the same, an OLED display panel having the OLED pixel structure, and an OLED display device having the OLED display panel. An OLED pixel structure comprises a plurality of sub-pixel units. Each of said sub-pixel units comprises: a first electrode, an organic material functional layer and a second electrode arranged in that order on said substrate plate; and a flat layer arranged between said substrate plate and said first electrode; wherein, a surface of said flat layer away from said substrate plate has a recess of arc shape; and said first electrode is located within said recess such that said first electrode, said organic material functional layer and said second electrode each has an arc shape corresponding to the arc shape of said recess. With these technical solutions according to the present disclosure, these problems of the narrow angle of view, and of different strengths and colors of the light as being viewed from different viewing angles, can be alleviated.



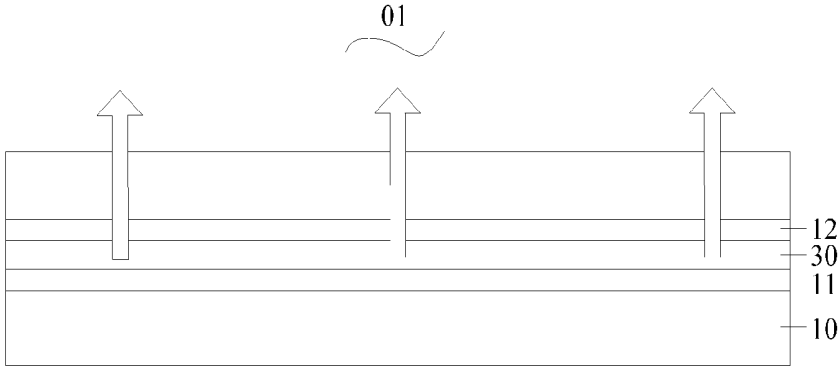


FIG. 1

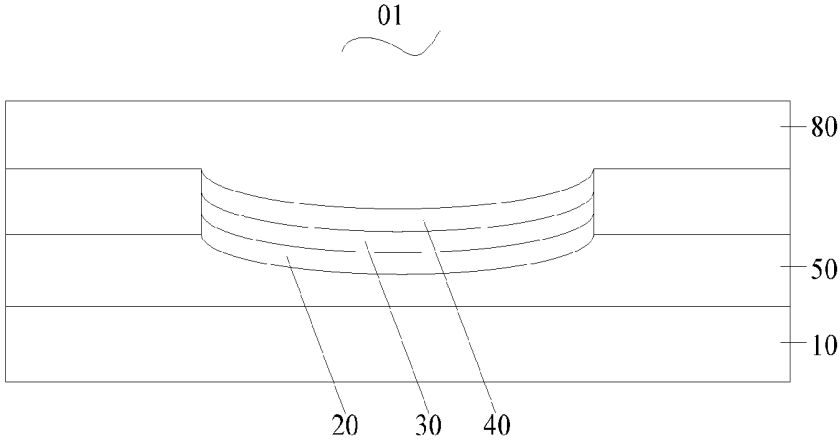


FIG. 2

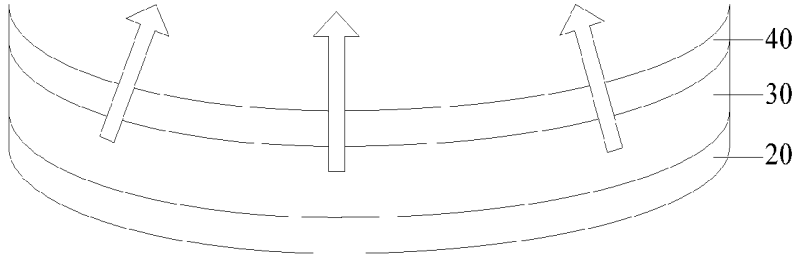


FIG. 3

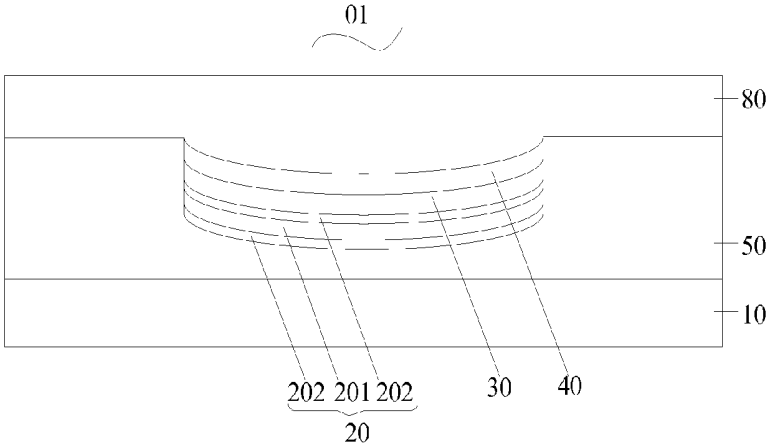


FIG. 4

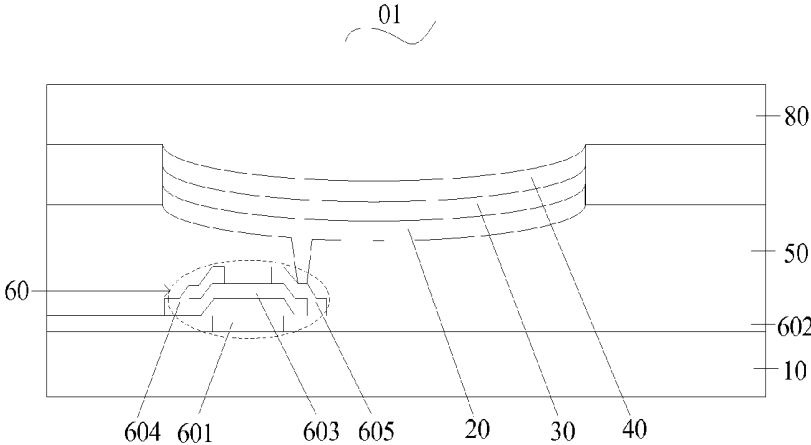


FIG. 5

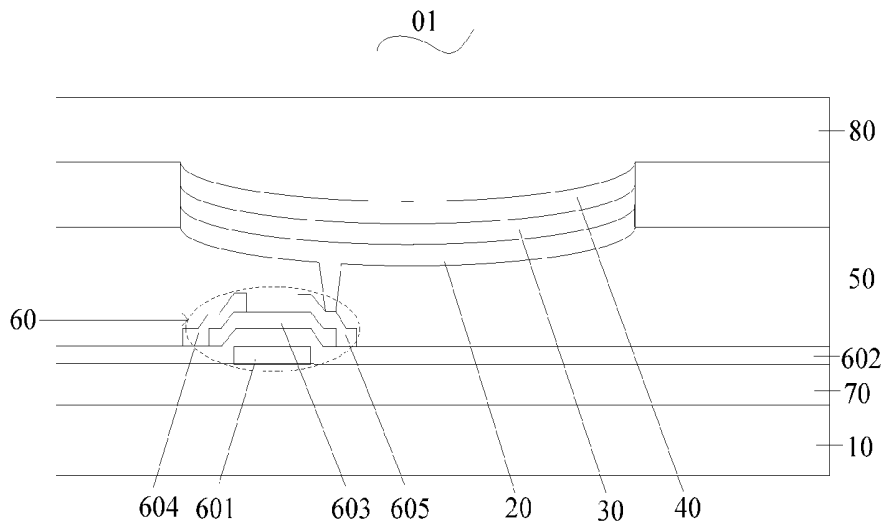


FIG. 6

to form a flat layer 50 on a substrate plate 10, wherein, within a region of each of the sub-pixel units corresponding to a first electrode 20, a surface of the flat layer 50 away from the substrate plate 10 has a recessed arc shape S10

▼
to form the first electrode 20, an organic material functional layer 30 and a second electrode 40 in that order, within the region of each of the sub-pixel units on the substrate plate 10 formed with the flat layer 50 S11

FIG. 7

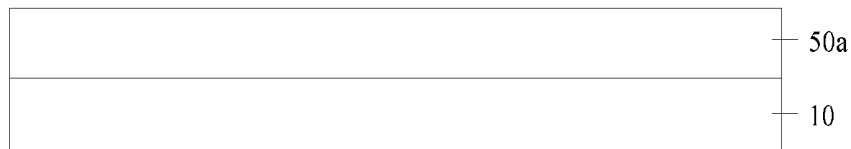


FIG. 8

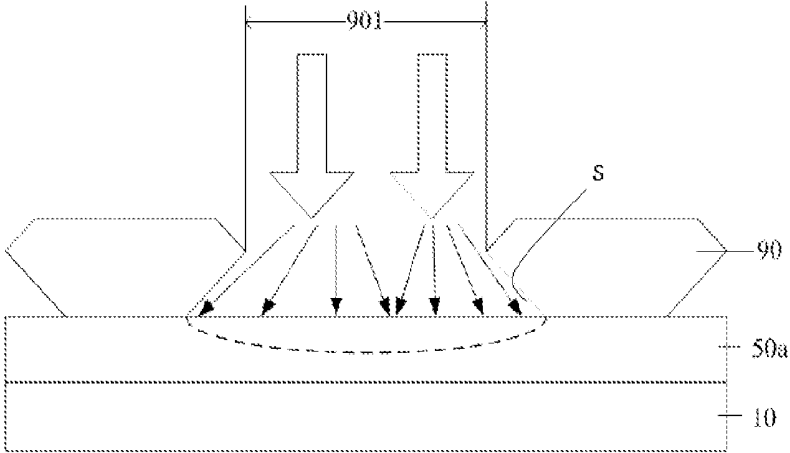


FIG. 9

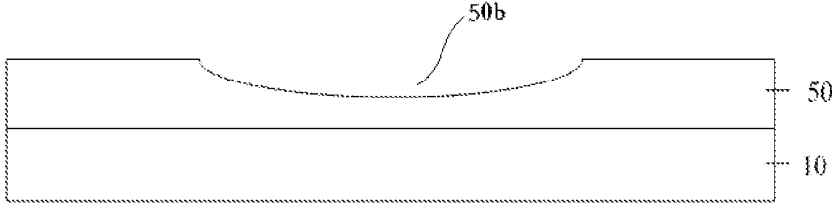


FIG. 10

**OLED PIXEL STRUCTURE AND METHOD
FOR MANUFACTURING THE SAME, OLED
DISPLAY PANEL AND OLED DISPLAY
DEVICE**

BACKGROUND OF THE INVENTION

[0001] 1. Field of the Invention

[0002] The disclosure relates to the field of display technology, and particularly to an OLED pixel structure and a method for manufacturing the same, an OLED display panel and an OLED display device having the OLED display panel.

[0003] 2. Description of the Related Art

[0004] Organic Light Emitting Diode (OLED for short) is an organic thin film electroluminescence device which has advantages of simple manufacture process, low cost, flexibility of the structure, wide angle of view, etc. As a result, the displaying technology employing an Organic Light Emitting Diode has been an important displaying technology.

[0005] Whether a top emission OLED pixel structure or a bottom emission OLED pixel structure, however, it will encounter some degree of microcavity effect. Microcavity effect mainly refers to that photon densities at different energy states are reallocated such that light with certain wavelength will be emitted at certain angle once it conforms to a resonant cavity mode. Especially, as to the top emission OLED pixel structure, an electrode that is near the substrate has a higher reflectivity, while an electrode that is away from the substrate usually employs a translucent metal thin film which will increase reflection of light to generate interference among multiple photon beams between the two electrodes so as to achieve a greater microcavity effect.

[0006] For example, as shown in FIG. 1, a top emission OLED pixel structure 01 is taken as an example, and it comprises a reflective anode 11, an organic material functional layer 30, a translucent metal cathode 12 arranged in that order on a substrate 10. Both an interface between the translucent metal cathode 12 and the organic material functional layer 30 and an interface between the reflective anode 11 and the organic material functional layer 30 are planar. And, the reflective anode 11, the organic material functional layer 30 and the translucent metal cathode 12 constitute a microcavity.

[0007] With the abovementioned structure, directions of emission of the interfered photon beams generated due to the microcavity effect are almost vertical ones, which results in these problems of the narrow angle of view, and different strengths and colors of the light as being viewed from different viewing angles.

SUMMARY

[0008] According to embodiments of the present disclosure, there are provided an OLED pixel structure and a method for manufacturing the same, an OLED display panel and an OLED display device having the OLED display panel., by which these problems of the narrow angle of view, and different strengths and colors of the light as being viewed from different viewing angles, can be alleviated.

[0009] In accordance with an aspect of the embodiments of the present disclosure, there is provided an OLED pixel structure comprising a substrate plate and a plurality of sub-pixel units, wherein each of the sub-pixel units comprises: a first electrode, an organic material functional layer and a second electrode arranged in that order on the substrate plate; and a flat layer arranged between the substrate plate and the first

electrode; wherein, a surface of the flat layer away from the substrate plate has a recess of arc shape; and the first electrode is located within the recess such that the first electrode, the organic material functional layer and the second electrode each has an arc shape corresponding to the arc shape of the recess.

[0010] In one embodiment, the flat layer has a thickness of 2 μm ~5 μm .

[0011] In one embodiment, the flat layer is electrically insulated from the first electrode.

[0012] In one embodiment, a material for the flat layer comprises polyimide. In one embodiment, the first electrode comprises an opaque metal layer and layers of tin indium oxide arranged at both sides of the opaque metal layer. Moreover, the second electrode comprises a silver conductive layer.

[0013] In one embodiment, the sub-pixel units further comprises a thin-film transistor arranged between the substrate plate and the flat layer.

[0014] In one embodiment, the first electrode is electrically connected with a drain of the thin-film transistor, via a through hole formed on the flat layer.

[0015] In one embodiment, the OLED pixel structure further comprises a buffer layer arranged between the substrate plate and the thin-film transistor and contacted the substrate plate.

[0016] In accordance with another aspect of the embodiments of the present disclosure, there is provided a method for manufacturing an OLED pixel structure comprising a plurality of sub-pixel units, the method comprises steps of:

[0017] forming a flat layer on a substrate plate, and, forming, on a region corresponding to a first electrode of each of the plurality of sub-pixel units, a recess of arc shape on a surface of the flat layer away from the substrate plate; and

[0018] forming the first electrode, an organic material functional layer and a second electrode in that order on each recess such that the first electrode, the organic material functional layer and the second electrode each has an arc shape corresponding to the arc shape of the recess.

[0019] In one embodiment, the step of forming the flat layer on the substrate plate comprises: forming a flat layer thin film on the substrate plate; and placing a mask plate having an opening above the surface of the flat layer thin film, dry etching the surface of the flat layer thin film by using a plasma coming into the opening to form the recess, wherein an area of the opening at a side near the flat layer thin film is greater than an area of the opening at a side where the plasma comes into.

[0020] In one embodiment, each of the sub-pixel units further comprises a thin-film transistor arranged between the substrate plate and the flat layer; and the step of forming the flat layer on the substrate plate further comprises: forming a through-hole at the recess of the flat layer; and, the step of forming the first electrode on each recess further comprises: electrically connecting the first electrode with a drain of the thin-film transistor via the hole.

[0021] In one embodiment, the step of forming the first electrode on each recess further comprises: arranging, a layer of tin indium oxide, on the recess; arranging an opaque metal layer on the layer of tin indium oxide; and arranging, another layer of tin indium oxide, on the opaque metal layer; wherein, the translucent metal layer and the two layers of tin indium oxide each has an arc shape corresponding to the arc shape of the recess.

[0022] In accordance with yet another aspect of the embodiments of the present disclosure, there is provided an OLED display panel comprising the above mentioned OLED pixel structure.

[0023] In accordance with still another aspect of the embodiments of the present disclosure, there is provided an OLED display device comprising the above mentioned OLED display panel.

BRIEF DESCRIPTION OF THE DRAWINGS

[0024] In order to describe embodiments of the present invention or technical solutions in the prior art more clearly, accompanying drawings required for describing the embodiments or the prior art will be simply explained as below. Apparently, the accompanying drawings for the following description are only some embodiments of the present invention. Those skilled in the art also could derive other accompanying drawings from these accompanying drawings without making a creative work.

[0025] FIG. 1 is a schematic structural view showing an OLED pixel structure in the prior art;

[0026] FIG. 2 is a first schematic structural view showing an OLED pixel structure according to an embodiment of the present disclosure;

[0027] FIG. 3 is a schematic diagram showing interference, among multiple photon beams from multiple directions, generated by an OLED pixel structure according to an embodiment of the present disclosure;

[0028] FIG. 4 is a second schematic structural view showing an OLED pixel structure according to an embodiment of the present disclosure;

[0029] FIG. 5 is a schematic structural view showing an OLED pixel structure, including a thin-film transistor, according to an embodiment of the present disclosure;

[0030] FIG. 6 is a schematic structural view showing an OLED pixel structure, including a buffer layer, according to an embodiment of the present disclosure;

[0031] FIG. 7 is a schematic flow view showing a method of manufacturing an OLED pixel structure according to an embodiment of the present disclosure; and

[0032] FIGS. 8-10 are schematic flows view showing these procedures of forming an arc-shaped flat layer having a recess on a surface thereof, according to an embodiment of the present disclosure.

REFERENCE SYMBOLS IN THE DRAWINGS

[0033] 01—OLED pixel structure, 10—substrate plate, 11—reflective anode, 12—translucent metal cathode, 20—first electrode, 201—opaque metal layer, 202—layer of tin indium oxide, 30—organic material functional layer, 40—second electrode, 50—flat layer, 50a—flat layer thin film, 60—thin-film transistor, 601—gate, 602—gate insulating layer, 603—semiconductor active layer, 604—source, 605—drain, 70—buffer layer, 80—encapsulated layer, 90—mask plate, 901—opening on the mask plate.

DETAILED DESCRIPTION OF THE EMBODIMENTS

[0034] A thorough and complete description of technical solutions of embodiments of the disclosure will be made as below with reference to these embodiments of the present disclosure taken in conjunction with the accompanying drawings. The embodiments set forth herein, however, merely are

some, but not all, of the embodiments of the present disclosure. On the basis of these embodiments set forth herein of the present disclosure, all other embodiments which are made without involving any inventive steps by those skilled in the art should be included within the scope of the present disclosure.

[0035] An OLED pixel structure 01 is provided in an embodiment of the present disclosure. The OLED pixel structure 01 comprises a substrate plate 10 and a plurality of sub-pixel units. Each of the sub-pixel units comprises: a first electrode 20, an organic material functional layer 30 and a second electrode 40 provided in that order on the substrate plate 10; and a flat layer 50 arranged between the substrate plate 10 and the first electrode 20; wherein, a surface of the flat layer 50 away from the substrate plate 10 has a recess 50b (see FIG. 9) of arc shape; and the first electrode 20 is located within the recess such that the first electrode 20, the organic material functional layer 30 and the second electrode 40 each has an arc shape corresponding to the arc shape of the recess.

[0036] The first electrode 20 may be an opaque metal layer, while the second electrode 40 may be a translucent metal electrode.

[0037] It should be explained that, firstly, there is no limitation on material for forming the flat layer 50, as long as it can be formed with a recess of arc shape on its surface away from the substrate plate 10.

[0038] The flat layer 50 is insulated from the first electrode. If the material for forming the flat layer 50 is a conductive material, an insulation layer should be provided between the flat layer 50 and the first electrode 20. Specific arrangement may be made according to practical situation, which will not be described repeatedly herein.

[0039] Secondly, as the flat layer 50 is located below the first electrode 20, that is, the flat layer 50 at the lower place is prepared ahead and then the first electrode 20 at the upper place is formed, accordingly, if the upper surface of the flat layer 50 corresponding to the first electrode 20 has a recessed arc shape, the first electrode 20 formed on the flat layer accordingly has a recessed arc shape. For same reason, the organic material functional layer 30 and the second electrode 40 located over the first electrode 20 accordingly have recessed arc shapes.

[0040] Thirdly, the first electrode 20 may be cathode or anode, while the second electrode 40 may be anode or cathode. That is, the second electrode 40 is an anode if the first electrode 20 is a cathode, or, the second electrode 40 is a cathode if the first electrode 20 is an anode.

[0041] As to the organic material functional layer 30, it may comprise at least an electron transport layer, an emitting layer and a hole transport layer. Based on this, in order to enhance efficiencies of electron and hole injections to the emitting layer, the organic material functional layer 30 may further comprise an electron injection layer arranged between a cathode and the electron transport layer and a hole injection layer arranged between the hole transport layer and an anode.

[0042] Based on this, when an operating voltage is applied to the anode and the cathode, both the through-holes from the anode and the electrons from the cathode are injected into the light emitting layer. The holes and the electrons meet each other within the emitting layer to recombine an electron-hole pair and release energy. This energy is released as light, and then, lights of different colors is displayed as the light passed,

through different luminescent molecules. And, they are emitted evenly from both sides of the organic material functional layer 30.

[0043] In three sub-pixel units of one pixel unit, the light emitting layers may comprise red light emitting molecules, green light emitting molecules and blue light emitting molecules, respectively. Of course, the abovementioned light emitting layer may also comprise only white light emitting molecules. There is no limitation on this herein.

[0044] Fourthly, the OLED pixel structure 01 may be a passive matrix type pixel structure, or an active matrix type pixel structure. There is no limitation on this herein.

[0045] Fifthly, in the embodiments of the present disclosure, the adjacent first electrodes 20 may be separated by a pixel separator layer, which will not be described repeatedly herein.

[0046] Sixthly, these drawings accompanied with these embodiments of the present disclosure merely schematically show these patterned layers which are related to the conceptive point, while, these patterned layers which are not related to the conceptive point are shown in part or not shown.

[0047] Compared with the prior art, in the embodiments of the present disclosure, the surface of the flat layer 50 corresponding to the first electrode 20 but being away from the substrate plate 10 is configured to have a recessed arc shape, such that the surfaces of the first electrode 20 and the second electrode 40 each has a recessed arc shape. As a result, lights emitted from the light emitting layer of the organic material functional layer 30 may form interference among multiple photon beams from multiple directions between the first electrode 20 and the second electrode 40 such that an emission ratio of lights at different directions is increased, and accordingly, the angle of view of the OLED display device is increased. On the basis of this, strengths and colors of lights as being viewed at different viewing angles are improved.

[0048] In an alternative embodiment of the present disclosure, as shown in FIG. 4, the first electrode 20 may comprise an opaque metal layer 201 and layers 202 of tin indium oxide (ITO) arranged at both sides of the opaque metal layer. As a result, energy level of the first electrode 20 may match that of the organic material functional layer 30, which facilitates injection of carriers of the first electrode 20 into the light emitting layer of the organic material functional layer 30.

[0049] The second electrode 40 may be a silver conductive layer having a relatively small thickness.

[0050] In an alternative embodiment of the present disclosure, the flat layer 50 may have a thickness of 2 μm –5 μm . Accordingly, the OLED display device is not too thick to meet the thinning tendency in market, or too thin to prepare the flat layer 50 having a surface in recessed arc shape.

[0051] In an alternative embodiment of the present disclosure, material for forming the flat layer 50 may be polyimide (PI). This is because, the PI is not compact, and thus it is easy to etch a portion of the flat layer 50 of PI material that is needed to be recessed, so as to obtain a desired etching effect when the flat layer 50 having the surface in recessed arc shape is prepared by a dry etching.

[0052] Based on the above, in consideration of disadvantages of application of a passive matrix on a large-sized display device, preferably, the OLED pixel structure 01 according to embodiments of the present disclosure is a passive matrix type OLED pixel structure. That is, as shown in FIG. 5, each sub-pixel unit in the OLED pixel structure 01 further

comprises a thin-film transistor 60 arranged between the substrate plate 10 and the flat layer 50.

[0053] The thin-film transistor 60 comprises a gate 601, a gate insulating layer 602, a semiconductor active layer 603, a source 604 and a drain 605. The thin-film transistor 60 may be either a top gate type or a bottom gate type.

[0054] Of course, the OLED pixel structure 01 further comprises grid lines and grid line leads (not shown in Figures) electrically connected to the gate 601, data line and data line leads (not shown in Figs) electrically connected to the source 604, etc.

[0055] Moreover, if the drain 605 is electrically connected with the second electrode 40, the second electrode 40 should be passed through the organic material functional layer 30 and the first electrode 20 to electrically connect to the drain 605. In this way, on one hand, a short circuit may occur between the second electrode 40 and the first electrode 20, and on the other hand, a relatively complicated preparation process is led due to the specificity of material of the organic material functional layer 30. Considering the above, in the embodiments of the present disclosure, the drain 605 is preferably electrically connected with the first electrode 20 and the first electrode 20 is electrically connected with the drain 605 of the thin-film transistor through the through hole in the flat layer 50.

[0056] In an alternative embodiment of the present disclosure, as shown in FIG. 6, the OLED pixel structure 01 further comprises a buffer layer 70 arranged between the substrate plate 10 and the thin-film transistor 60, the buffer layer 70 being contacted with the substrate plate 10.

[0057] The buffer layer 70 may be in a configuration of monolayer or multilayer, and, may be made from SiN (silicon nitride), SiOx (silicon oxides), etc.

[0058] The use of the buffer layer 70 not only facilitates to improve surface smoothness and adhesive force of the substrate plate 10, but also helps to improve resistance to water and oxygen permeability.

[0059] It should be explained that, in the embodiments of the present disclosure, due to specificity of the material for forming the organic material functional layer 30, the OLED pixel structure 01 may further comprise an encapsulated layer 80 for encapsulation of organic materials. The encapsulated layer 80 may be a thin film encapsulation or a substrate plate encapsulation, and there is no limitation on this herein.

[0060] A method for manufacturing an OLED pixel structure comprising a plurality of sub-pixel units is also provided in the embodiments of the present disclosure. As shown in FIG. 7, the method comprises the following steps.

[0061] S10, referring to FIG. 2, to form a flat layer 50 on a substrate plate 10, wherein, within a region of each of the sub-pixel units corresponding to a first electrode 20, a surface of the flat layer 50 away from the substrate plate 10 has a recessed arc shape.

[0062] S11, referring to FIG. 2, to form the first electrode 20, an organic material functional layer 30 and a second electrode 40 in that order, within the region of each of the sub-pixel units on the substrate plate 10 formed with the flat layer 50, such that the first electrode, the organic material functional layer and the second electrode each has an arc shape corresponding to the arc shape of the recess.

[0063] The first electrode 20 may be an opaque metal layer, while the second electrode 40 may be a translucent metal electrode.

[0064] It should be explained that, firstly, there is no limitation on the formation method of the flat layer 50 having a surface of the recessed arc shape.

[0065] Secondly, since the flat layer 50 at the lower place is prepared ahead and then the first electrode 20 is formed on the flat layer, when the upper surface of the flat layer 50 corresponding to the first electrode 20 has a recessed arc shape, the first electrode 20 formed on the flat layer accordingly has a recessed arc shape. For same reason, the organic material functional layer 30 and the second electrode 40 located over the first electrode 20 accordingly have recessed arc shapes.

[0066] Compared with the prior art, in the embodiments of the present disclosure, the surface of the flat layer 50 corresponding to the first electrode 20 but being away from the substrate plate 10 is configured to have a recessed arc shape, such that the surfaces of the first electrode 20 and the second electrode 40 each has a recessed arc shape. As a result, lights emitted from the emitting layer of the organic material functional layer 30 may form interference among multiple photon beams from multiple directions between the first electrode 20 and the second electrode 40 such that an emission ratio of lights at different directions is increased, and accordingly, the angle of view of the OLED display device is increased. On the basis of this, strengths and colors of lights as being viewed at different viewing angles are improved.

[0067] In an alternative embodiment of the present disclosure, the abovementioned step S10 may further comprises the followings.

[0068] S101, referring to FIG. 8, to form a flat layer thin film 50a on the substrate plate 10.

[0069] Because the structure of PI is not compact, once the flat layer 50 having the surface of recessed arc shape is prepared by a dry etching, it is easy to etch a portion of the flat layer 50 of PI material that is needed to be recessed, so as to obtain a desired etching effect. Material for forming the flat layer thin film 50a may be PI material.

[0070] S102, referring to FIG. 9, by a shielding effect of a patterned mask plate 90, a drying etching is performed on the flat layer thin film 50a which is not shielded completely by the mask plate 90, such that the flat layer 50 having a surface of the recessed arc shape, as shown in FIG. 10, is formed.

[0071] The opening in the mask plate 901 corresponds to a predetermined region of the sub-pixel unit, and, an area of the opening 901 at a side near the flat layer thin film 50a is greater than an area of the opening 901 at the other side away from the flat layer thin film 50a (where the plasma comes into).

[0072] It should be explained that, the drying etching is a technology of etching a thin film using a plasma. Based on this, in the embodiments of the present disclosure, the mask plate 90 having a shape shown in FIG. 9 is used as the shield, that is, a shadow effect of the mask plate 90 is used.

[0073] Because the plasma, within a region (in which a space S is presented between the mask plate 90 and the flat layer thin film 50a) vertically blocked by the mask plate 90, has a relatively smaller concentration, the closer to the margin of the mask plate 90, the poorer is the etching effect, i.e., a relatively lesser depth is obtained. Meanwhile, because the plasma, within a region rightly against the opening on the mask plate 901, has a relatively greater concentration due to direct access of the plasma, the etching effect is greater there, accordingly, a relatively greater depth is obtained.

[0074] Secondly, the predetermined region is a region having a depth greater than certain recessed depth, within the region of each of the sub-pixel units corresponding to the first electrode 20.

[0075] Thirdly, the dry etching performed on the flat layer thin film 50a which is not shielded completely by the mask plate 90 means that the plasma that comes into at the opening 901 may etch the flat layer thin film 50a as long as a space S is presented between the mask plate 90 and the flat layer thin film 50a.

[0076] Referring to FIG. 4, the first electrode 20 may comprise an opaque metal layer 201 and layers 202 of tin indium oxide (ITO) arranged at both sides of the opaque metal layer. The second electrode 40 may be a silver conductive layer having a relatively small thickness.

[0077] In the abovementioned method, the flat layer 50 may have a thickness of 2 μm ~5 μm . Accordingly, the OLED display device is not be too thick to meet the thinning tendency in market, or too thin to prepare the flat layer 50 having a surface of recessed arc shape.

[0078] In the abovementioned method, material for forming the flat layer 50 may be polyimide (PI). Based on the above, in consideration of disadvantages of application of a passive matrix on a large-sized display device, preferably, the OLED pixel structure 01 according to embodiments of the present disclosure is a passive matrix type OLED pixel structure. That is, as shown in FIG. 5, each sub-pixel unit in the OLED pixel structure 01 further comprises a thin-film transistor 60 arranged between the substrate plate 10 and the flat layer 50.

[0079] The thin-film transistor 60 comprises a gate 601, a gate insulating layer 602, a semiconductor active layer 603, a source 604 and a drain 605. The thin-film transistor 60 may be either a top gate type or a bottom gate type.

[0080] Moreover, the first electrode 20 is electrically connected with the drain 605 of the thin-film transistor through the through hole in the flat layer 50.

[0081] Referring to FIG. 6, the method may further comprise a step of forming a buffer layer 70 between the substrate plate 10 and the thin-film transistor 60, wherein the buffer layer 70 is in contact with the substrate plate 10. The use of the buffer layer 70 not only facilitates to improve surface smoothness and adhesive force of the substrate plate 10, but also helps to improve resistance to water and oxygen permeability.

[0082] In accordance with yet another aspect of the embodiments of the present disclosure, there is provided an OLED display panel comprising the abovementioned OLED pixel structure.

[0083] In accordance with still another aspect of the embodiments of the present disclosure, there is provided an OLED display device comprising the abovementioned OLED display panel.

[0084] The above description is merely used to illustrate specific embodiments of the present disclosure, but not to limit the present disclosure. It will be understood by those skilled in the art that various changes and modifications made within principles and spirit of the present disclosure should be included within the scope of the present disclosure. The scope of the present disclosure is defined in the appended claims.

1. An OLED pixel structure comprising a substrate plate and a plurality of sub-pixel units, each of said plurality of sub-pixel units comprising:

a first electrode, an organic material functional layer and a second electrode arranged in that order on said substrate plate; and
 a flat layer arranged between said substrate plate and said first electrode;
 wherein,
 a surface of said flat layer away from said substrate plate has a recess of arc shape; and
 said first electrode is located within said recess such that said first electrode, said organic material functional layer and said second electrode each has an arc shape corresponding to the arc shape of said recess.

2. The OLED pixel structure of claim 1, wherein, said flat layer has a thickness of 2 μm –5 μm .

3. The OLED pixel structure of claim 1, wherein, said flat layer is electrically insulated from said first electrode.

4. The OLED pixel structure of claim 3, wherein, a material for forming said flat layer comprises polyimide.

5. The OLED pixel structure of claim 1, wherein, said first electrode comprises an opaque metal layer and layers of tin indium oxide arranged at both sides of said opaque metal layer.

6. The OLED pixel structure of claim 5, wherein, said second electrode comprises a silver conductive layer.

7. The OLED pixel structure of claim 1, wherein, said sub-pixel units further comprises a thin-film transistor arranged between said substrate plate and said flat layer.

8. The OLED pixel structure of claim 7, wherein, said first electrode is electrically connected with a drain of said thin-film transistor, via a through hole formed in said flat layer.

9. The OLED pixel structure of claim 7, wherein, said OLED pixel structure further comprises a buffer layer arranged between said substrate plate and said thin-film transistor and contacted with said substrate plate.

10. A method for manufacturing an OLED pixel structure comprising a plurality of sub-pixel units, the method comprising steps of:
 forming a flat layer on a substrate plate, and, forming, at a region corresponding to a first electrode of each of the sub-pixel units, a recess of arc shape on a surface of said flat layer away from said substrate plate; and
 forming said first electrode, an organic material functional layer and a second electrode in that order on each recess

such that said first electrode, said organic material functional layer and said second electrode each has an arc shape corresponding to the arc shape of said recess.

11. The method of claim 10, wherein, the step of forming the flat layer on the substrate plate further comprises:
 forming a flat layer thin film on said substrate plate; and
 placing a mask plate having an opening above said surface of said flat layer thin film, and dry etching said surface of said flat layer thin film by using a plasma coming into said opening to form said recess, wherein an area of said opening at a side near said flat layer thin film is greater than an area of said opening at a side where the plasma comes into.

12. The method of claim 10, wherein,
 each of said plurality of sub-pixel units further comprises a thin-film transistor arranged between said substrate plate and said flat layer; and
 the step of forming the flat layer on the substrate plate further comprises: forming a through hole at said recess of said flat layer; and, the step of forming the first electrode on each recess further comprises: electrically connecting said first electrode with a drain of said thin-film transistor via said through hole.

13. The method of claim 10, wherein,
 the step of forming the first electrode on each recess further comprises:
 arranging a layer of tin indium oxide on said recess;
 arranging an opaque metal layer over said layer of tin indium oxide; and
 arranging another layer of tin indium oxide over said opaque metal layer;
 wherein, the translucent metal layer and the two layers of tin indium oxide each has an arc shape corresponding to the arc shape of said recess.

14. An OLED display panel comprising an OLED pixel structure of claim 1.

15. An OLED display device comprising an OLED display panel of claim 14.

16. An OLED display panel comprising an OLED pixel structure of claim 9.

17. An OLED display device comprising an OLED display panel of claim 16.

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专利名称(译)	OLED像素结构及其制造方法，OLED显示面板和OLED显示装置		
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摘要(译)

本公开的实施例涉及OLED像素结构及其制造方法，具有OLED像素结构的OLED显示面板，以及具有OLED显示面板的OLED显示装置。OLED像素结构包括多个子像素单元。每个所述子像素单元包括：第一电极，有机材料功能层和第二电极，依次设置在所述基板上；以及设置在所述基板和所述第一电极之间的平坦层；其中，所述平坦层远离所述基板的表面具有弧形凹槽；所述第一电极位于所述凹槽内，使得所述第一电极，所述有机材料功能层和所述第二电极各自具有与所述凹槽的弧形形状对应的弧形形状。利用根据本公开的这些技术方案，可以减轻从不同视角观察的窄视角以及不同强度和颜色的光的这些问题。

